

Refine Search

Search Results -

Terms	Documents
L31 and ("P-type" near5 channel)	2

Database:

US Pre-Grant Publication Full-Text Database
 US Patents Full-Text Database
 US OCR Full-Text Database
 EPO Abstracts Database
 JPO Abstracts Database
 Derwent World Patents Index
 IBM Technical Disclosure Bulletins

Search:

L32

Refine Search

Recall Text

Clear

Interrupt

Search History

 DATE: Thursday, August 11, 2005 [Printable Copy](#) [Create Case](#)

Set Name Query

side by side

Hit Count Set Name

result set

DB=USPT; PLUR=YES; OP=OR

<u>L32</u>	L31 and ("P-type" near5 channel)	2	<u>L32</u>
<u>L31</u>	("channel" near6 "intrinsic") and L30	2	<u>L31</u>
<u>L30</u>	L29 and tft	2	<u>L30</u>
<u>L29</u>	("Static random access" or "static ram") and 6166786	2	<u>L29</u>
<u>L28</u>	("Static random access" or "static ram") and 5920093	2	<u>L28</u>
<u>L27</u>	("Static random access" or "static ram") and 4809056	3	<u>L27</u>
<u>L26</u>	("Static random access" or "static ram") and 5089870	2	<u>L26</u>
<u>L25</u>	("Static random access" or "static ram") and 5898204	3	<u>L25</u>
<u>L24</u>	L23	0	<u>L24</u>
<u>L23</u>	("Static random access" or "static ram") and 5898204L22	0	<u>L23</u>
<u>L22</u>	5712501 and ("Static random access" or "static ram")	2	<u>L22</u>
<u>L21</u>	L20 and ("Static random access" or "static ram")	2	<u>L21</u>
<u>L20</u>	5812231	14	<u>L20</u>

DB=DWPI; PLUR=YES; OP=OR

<u>L19</u>	L18 and ("channel" near6 "intrinsic")	0	<u>L19</u>
<u>L18</u>	L17 and tft	126	<u>L18</u>
<u>L17</u>	"Static random access" or "static ram"	6090	<u>L17</u>
<i>DB=JPAB; PLUR=YES; OP=OR</i>			
<u>L16</u>	L15 and ("channel" near6 "intrinsic")	0	<u>L16</u>
<u>L15</u>	L14 and tft	17	<u>L15</u>
<u>L14</u>	L13 and tft	17	<u>L14</u>
<u>L13</u>	L12	992	<u>L13</u>
<u>L12</u>	"Static random access" or "static ram"	992	<u>L12</u>
<i>DB=EPAB; PLUR=YES; OP=OR</i>			
<u>L11</u>	L10 and ("channel" near6 "intrinsic")	0	<u>L11</u>
<u>L10</u>	L9 and tft	2	<u>L10</u>
<u>L9</u>	("P-type" near5 channel)	85	<u>L9</u>
<i>DB=PGPB; PLUR=YES; OP=OR</i>			
<u>L8</u>	L7 and ("P-type" near5 channel)	7	<u>L8</u>
<u>L7</u>	L6 and ("channel" near6 "intrinsic")	22	<u>L7</u>
<u>L6</u>	L5 and tft	542	<u>L6</u>
<u>L5</u>	"Static random access" or "static ram"	10148	<u>L5</u>
<i>DB=USPT; PLUR=YES; OP=OR</i>			
<u>L4</u>	L3 and ("P-type" near5 channel)	11	<u>L4</u>
<u>L3</u>	l2 and ("channel" near6 "intrinsic")	33	<u>L3</u>
<u>L2</u>	L1 and tft	902	<u>L2</u>
<u>L1</u>	"Static random access" or "static ram"	20651	<u>L1</u>

END OF SEARCH HISTORY